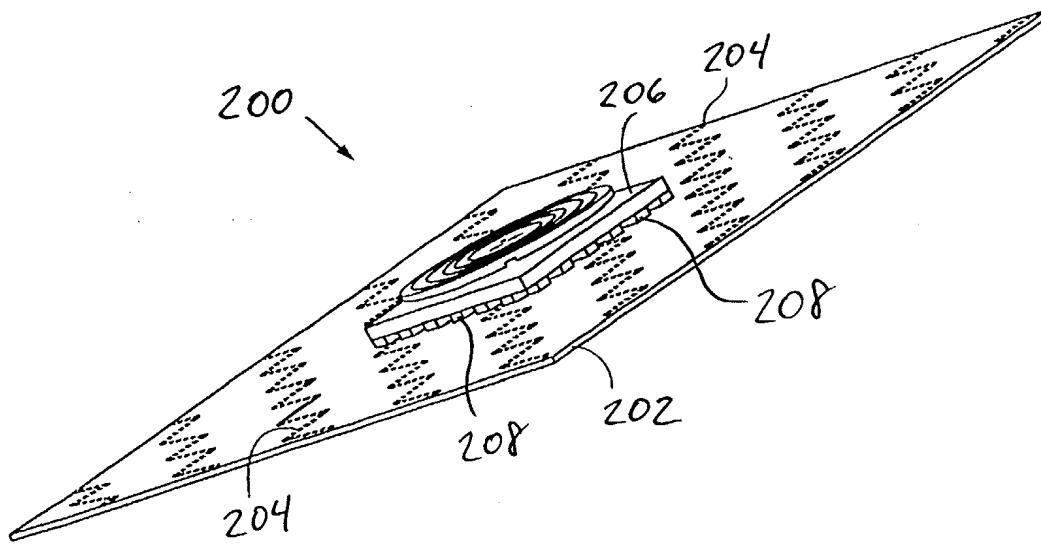


PRIOR ART

Fig. 1



PRIOR ART

Fig. 2

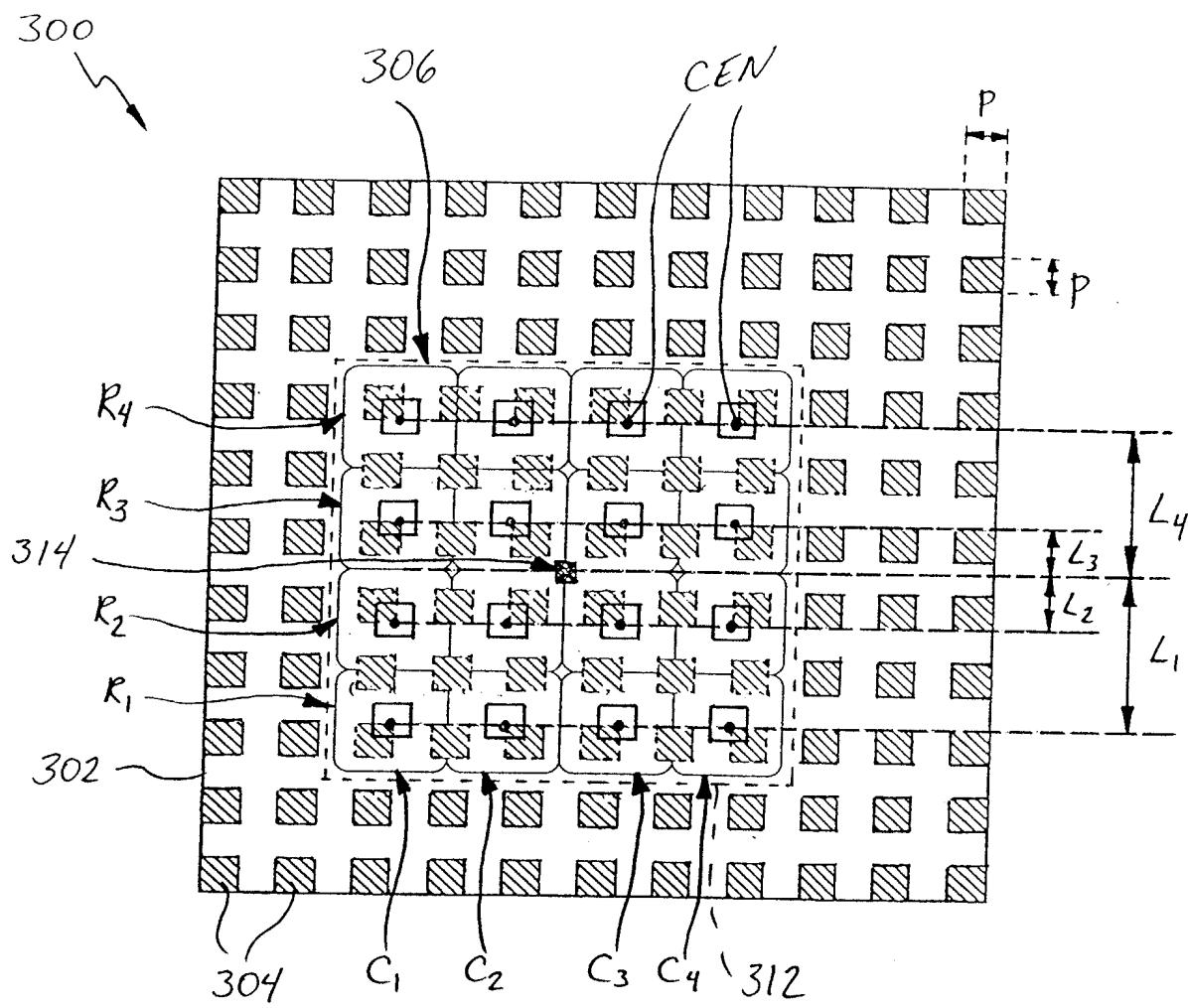


Fig. 3

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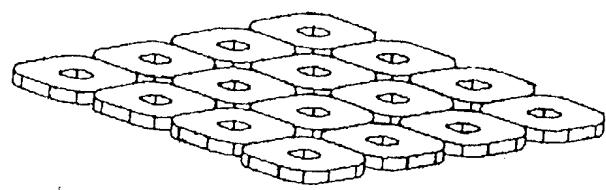


Fig. 4

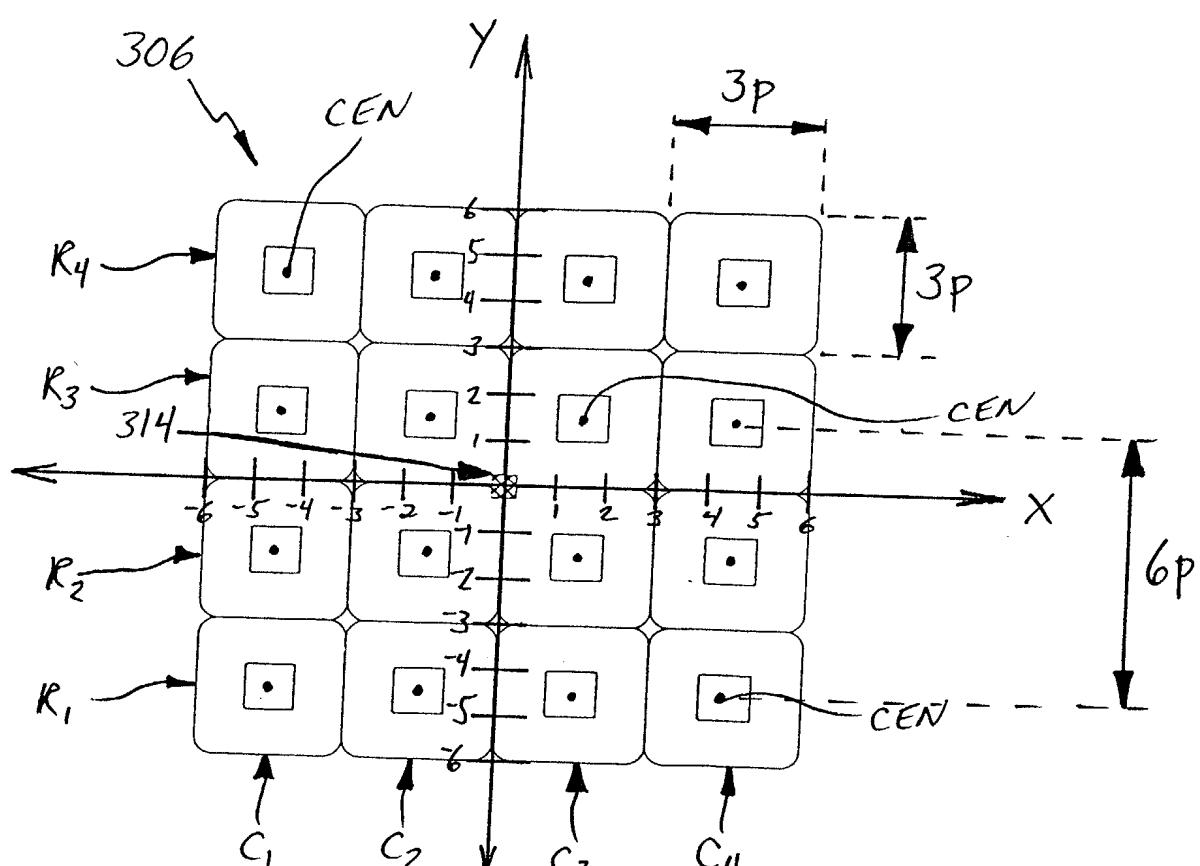


Fig. 5

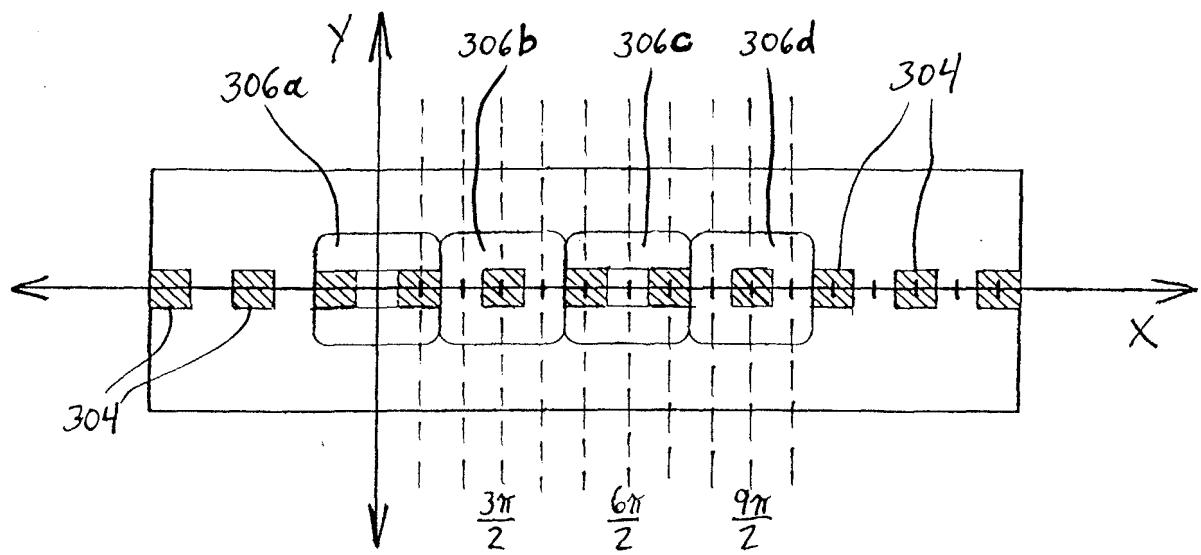


FIG. 5A

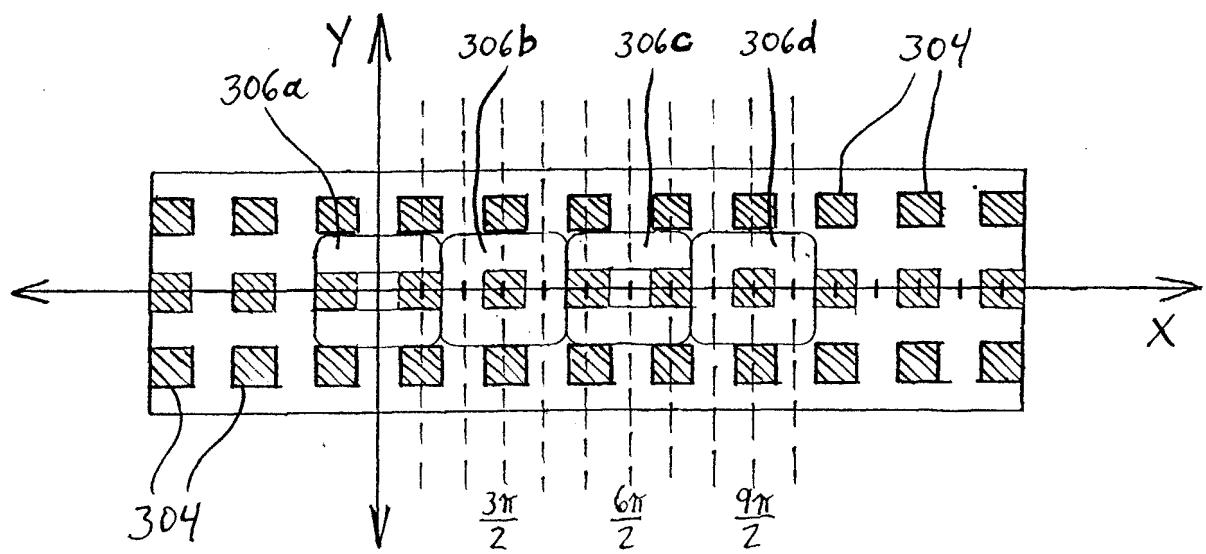


FIG. 5B

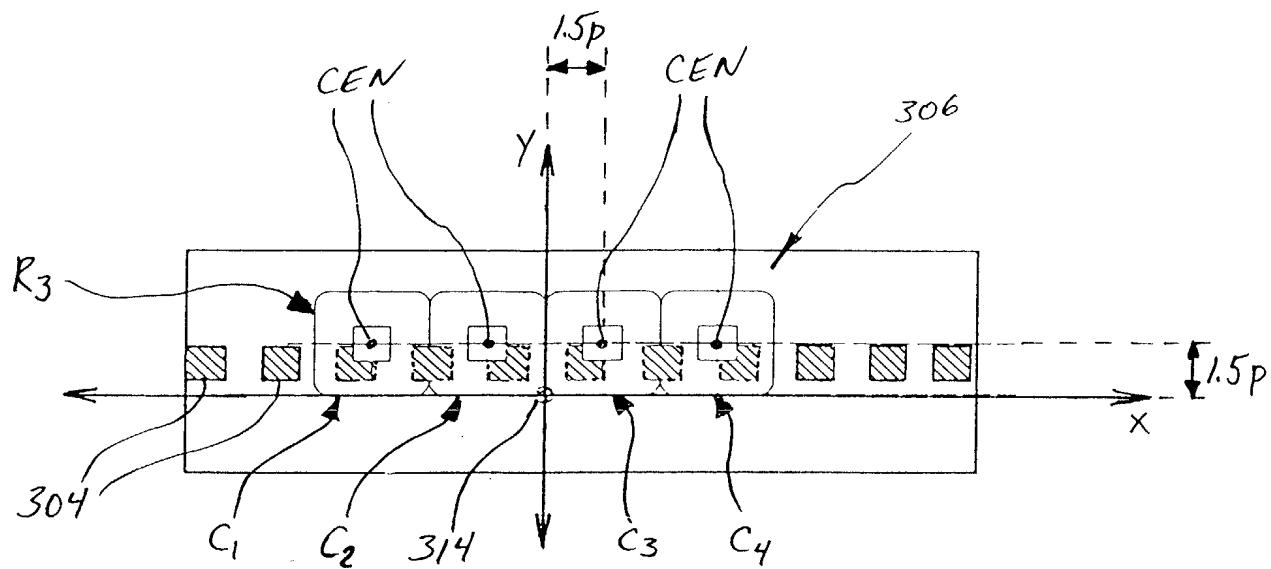


Fig. 6

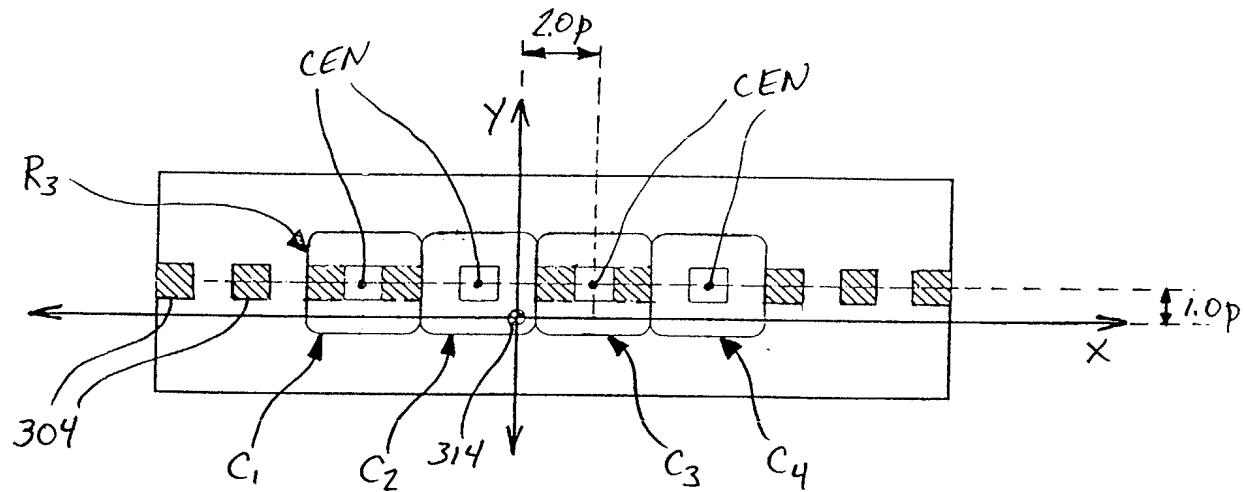


Fig. 7

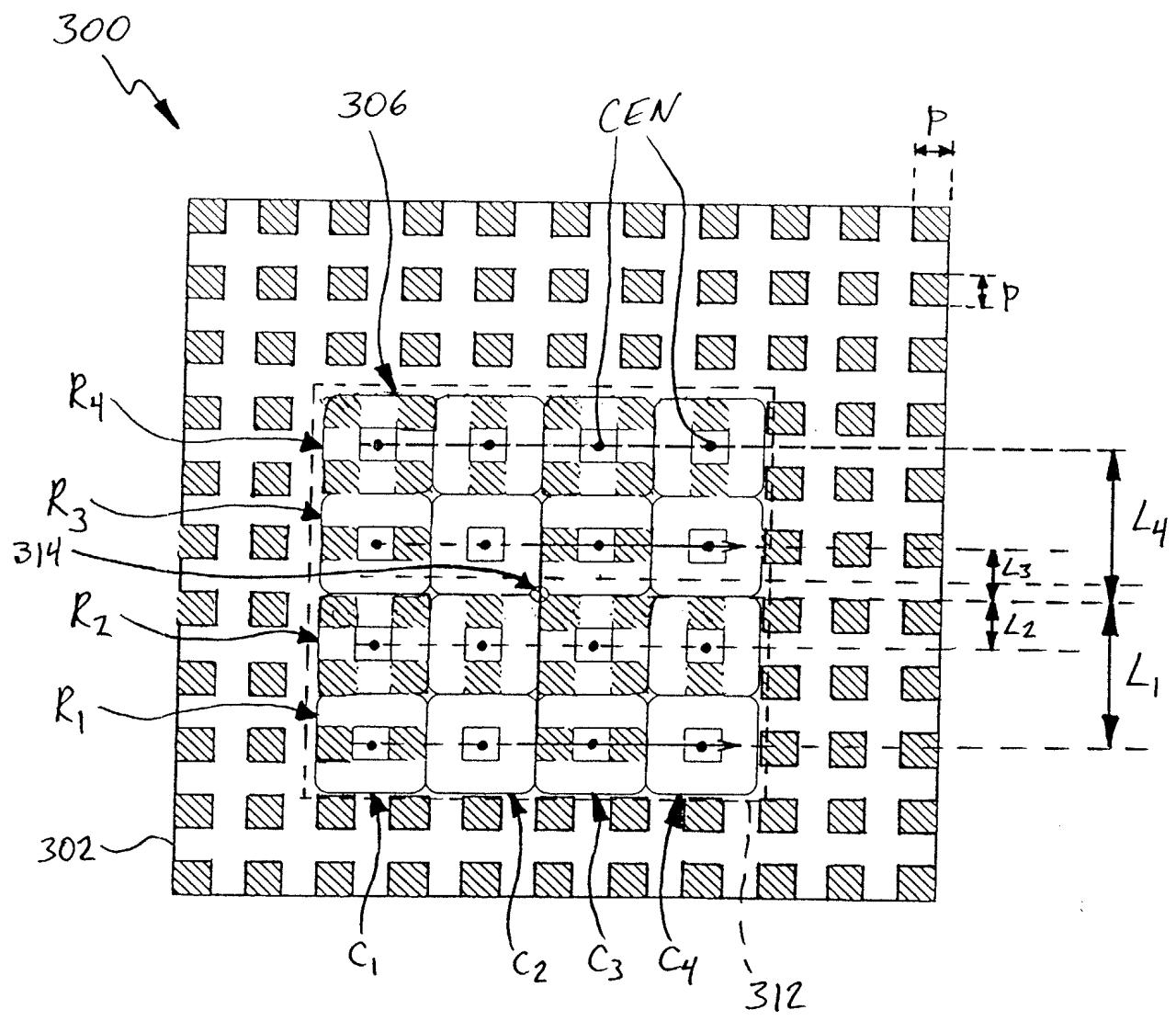


Fig. 8

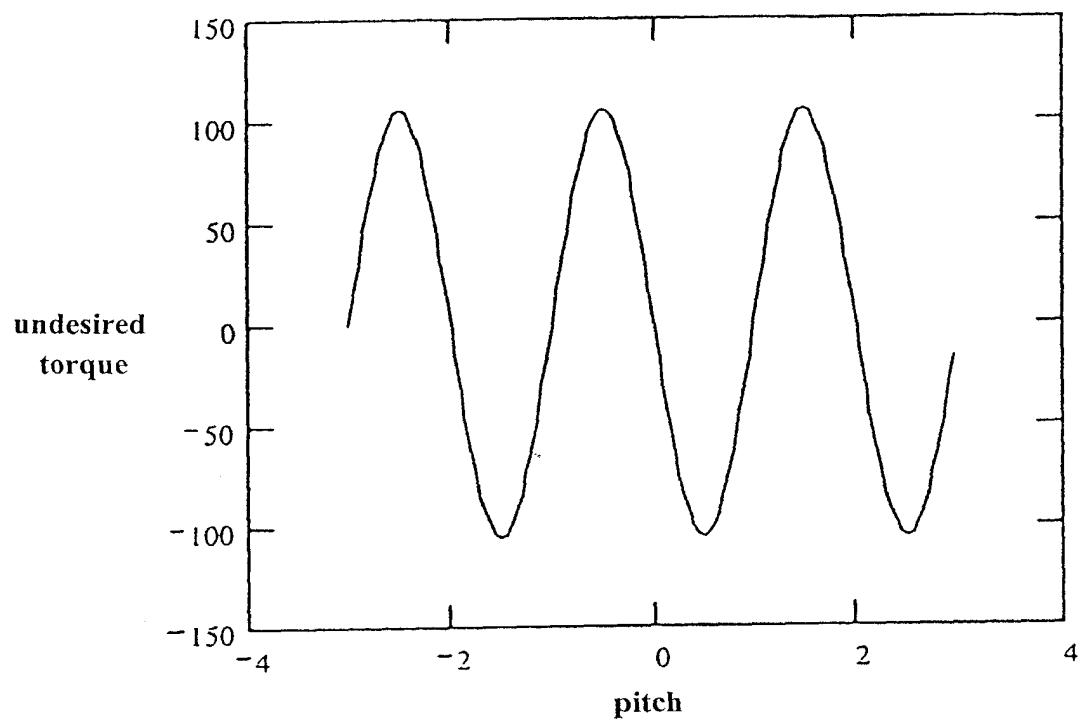


Fig. 9

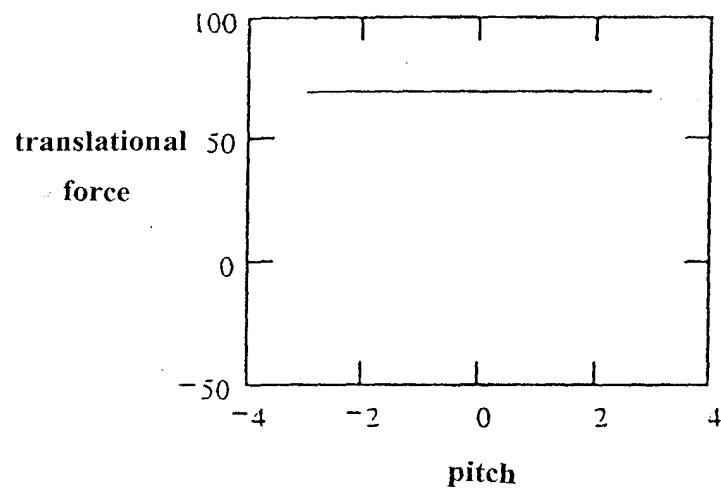


Fig. 10

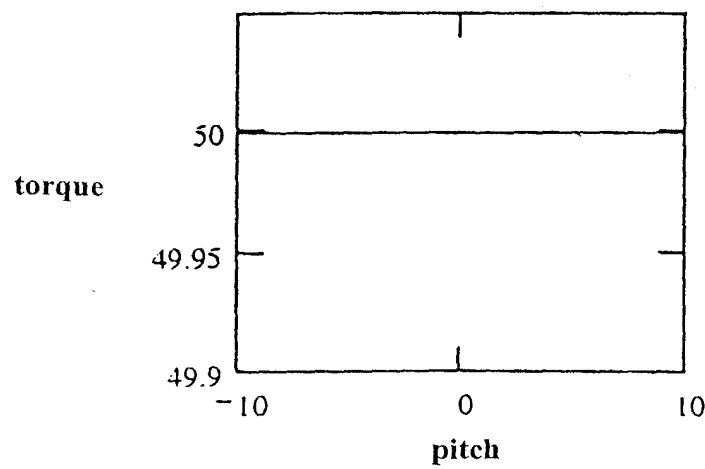


Fig. 11

Fig. 12A shows a block diagram of a digital-to-analog converter (DAC) system.

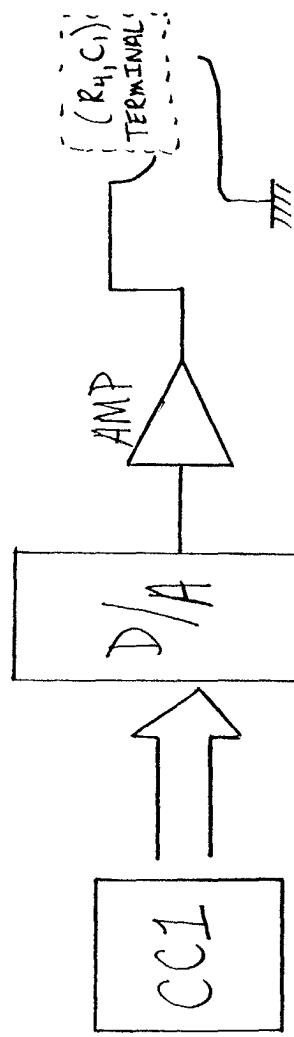


FIG. 12A

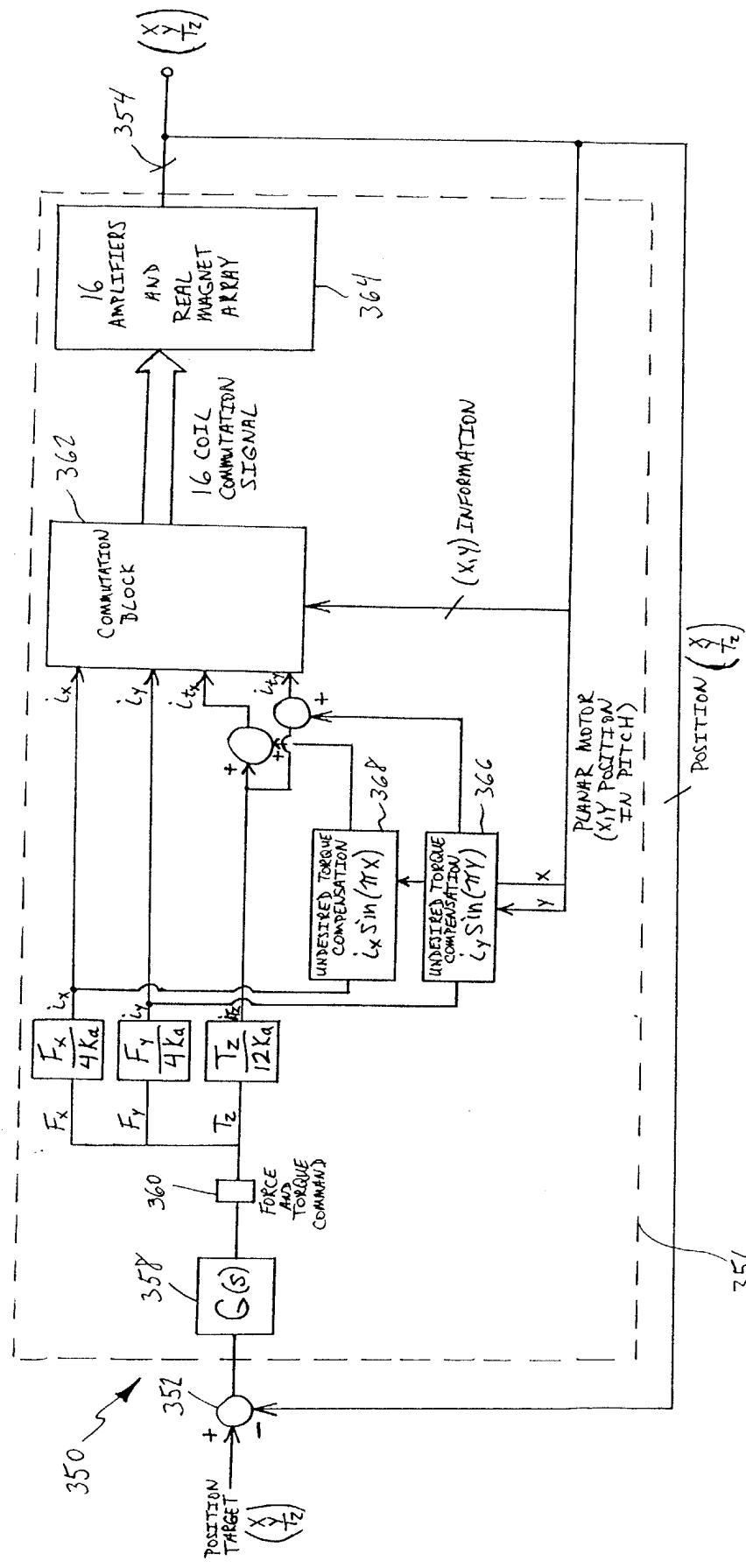


FIG. 12B

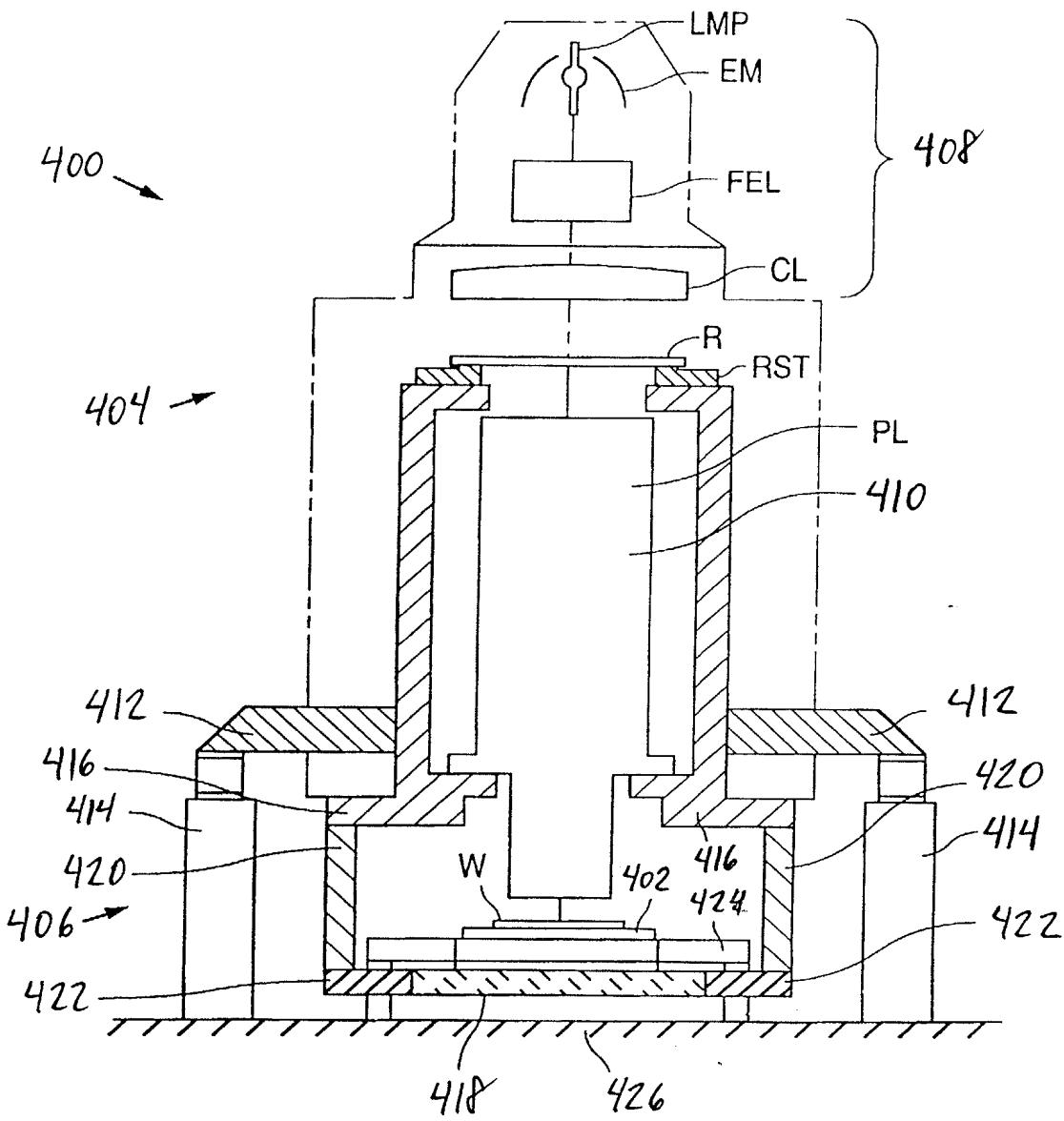


FIG. 13

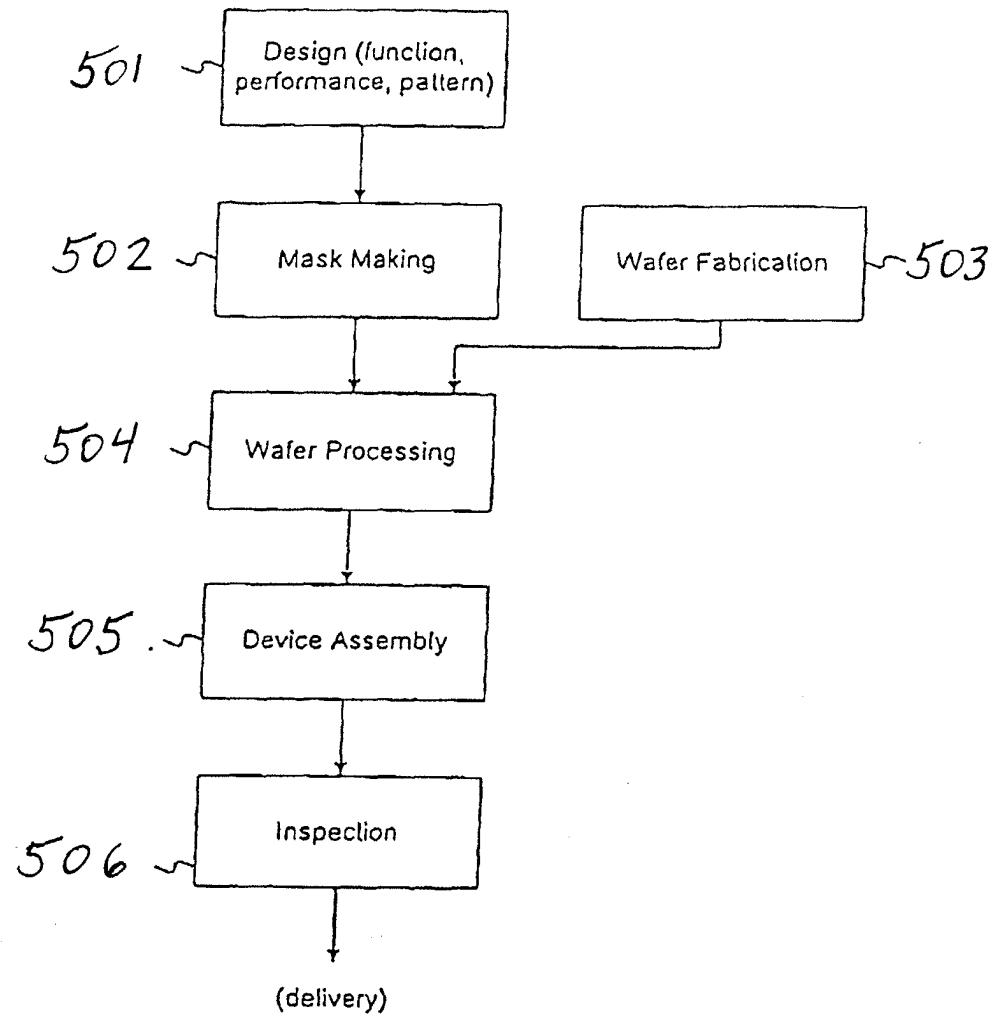


Fig. 14

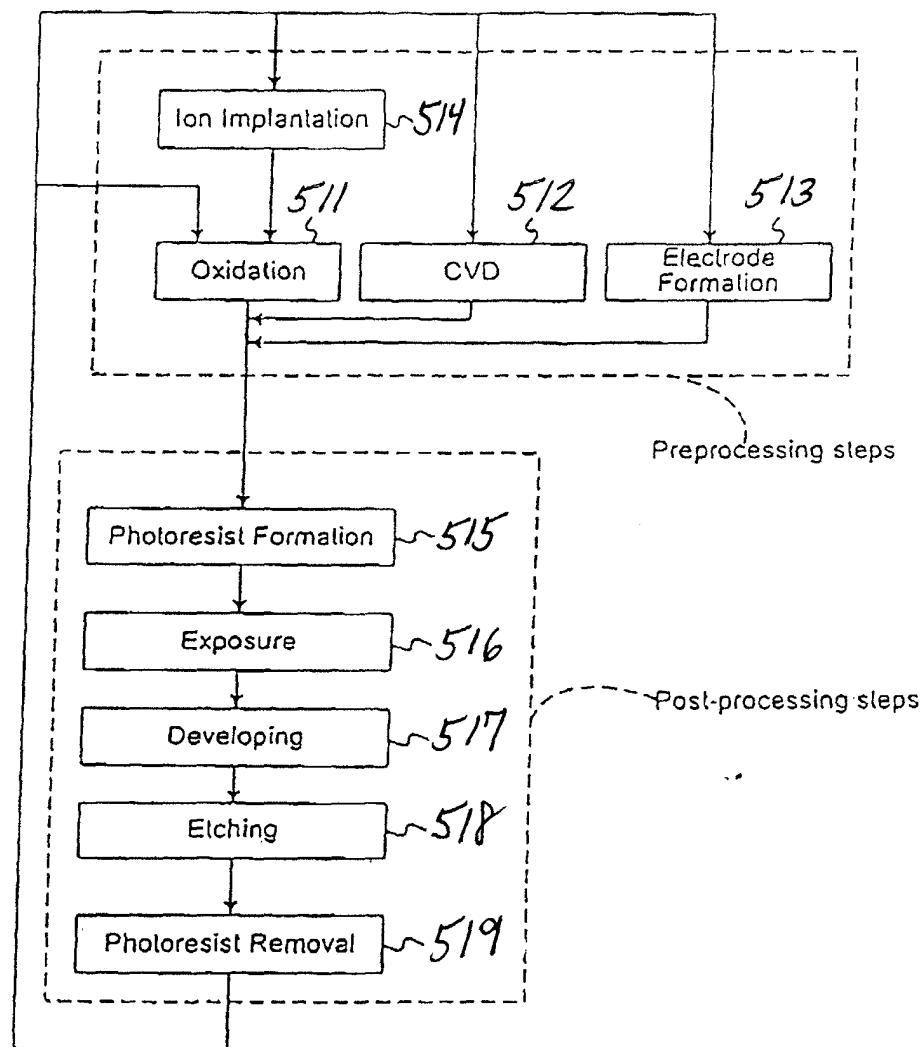


Fig. 15